

Notice of Allowability

Application No.

10/657,178

Examiner

My-Trang N. Ton

Applicant(s)

KONISHI, TAKANORI

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 10/12/04.
2. ☒ The allowed claim(s) is/are 1-10.
3. ☒ The drawings filed on 9/9/03 & 10/12/04 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some* c) ☐ None of the:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
- * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

Reasons for allowance

The following is an examiner's statement of reasons for allowance:

None of the prior art disclosed or suggested to show the particular structure and/or the particular operation recited in these claims namely: "a connection point between the first capacitor and the input wiring is closer to a connection point between the input terminal section and the input wiring than a connection point between the processing circuit section and the input wiring" and "a portion of the input wiring extending from the connection point with the first capacitor to the connection point with the input terminal section is formed by only a conductive pattern" as recited in claims 1 and 5; "a portion of the input wiring extending from a connection point with the first capacitor to a connection point with the resistor includes three or more conductive patterns which are respectively placed on both faces of the printed wiring board and two or more through holes or via holes which connect the conductive patterns placed on both of the faces of the printed wiring board" as recited in claims 2 and 6; "a portion of the input wiring extending from a connection point with the first capacitor to a connection point with the resistor is parallel to and in a vicinity of the grounding wiring" as recited in claims 3 and 7; "a connection point between the second capacitor and the input wiring is closer to a connection point between the transistor and the input wiring than the connection point between the first capacitor and the input wiring" and "a portion of the input wiring extending from the connection point with the first capacitor to the connection point with the second capacitor includes two or more conductive patterns which are respectively placed on both faces of the printed wiring board and one or more

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through holes or via holes which connect the conductive patterns placed on both of the faces of the printed wiring board” as recited in claims 4 and 8.

The prior art references submitted by the applicant on IDS forms received on 01/02/04 & 12/09/03 were reviewed and considered. However, these references lack the above limitations. Therefore, the claims are patentably distinct over all these prior art references of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

Any inquiry concerning this communication or earlier communications from the examiner should be directed to My-Trang N. Ton whose telephone number is 571-272-1754. The examiner can normally be reached on 7:00 a.m - 5:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



MY-TRANG NUTON
PRIMARY EXAMINER

October 28, 2004